



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of:)

Shunpei YAMAZAKI et al.)

Art Unit: Unassigned

Serial No. 09/761,641)

Examiner: Unassigned

Filed: 01/18/2001)

For: METHOD OF MANUFACTURING A)

SEMICONDUCTOR DEVICE)

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on 4/25/2001.

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the reference(s) listed on the attached Form PTO-1449 be made of record in the above-identified application. Copies of the references are submitted herewith in accordance with 37 C.F.R. 1.98(a).

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380. A duplicate copy of this sheet is attached.

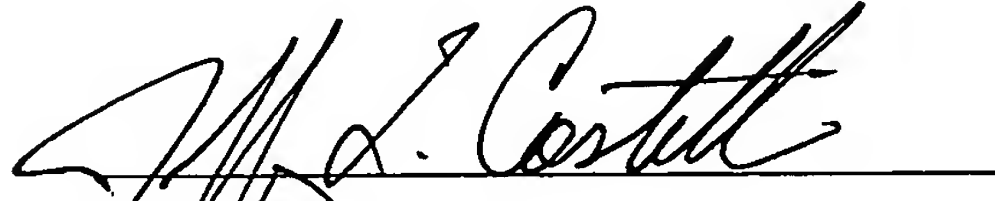
Respectfully submitted,

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World Patent No. 90/13148 and U.S. Patent Nos. 5,247,190 and 5,399,502 are listed in the patent family of Japanese Patent Laid-Open No. 10-092576.

Respectfully submitted,



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